

# *a lot of tiles (trivial scan)*


2232001

michael winter

(cdmx, mx and nashville, usa; 2018)


part - hl 4 - 2232001

hl 4  
(low noise)



⑨

hl4 (ln)




⑰

hl4 (ln)



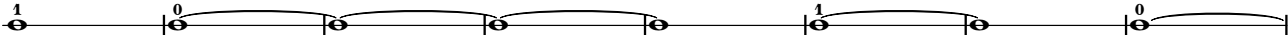
⑳

hl4 (ln)




㉓

hl4 (ln)




㉑

hl4 (ln)




㉙

hl4 (ln)



㉗

hl4 (ln)



65

73

(81)

The diagram shows a horizontal line with 11 points labeled  $x_0$  through  $x_{10}$ . Above each point is a binary digit:  $x_0$  has 0,  $x_1$  has 1,  $x_2$  has 0,  $x_3$  has 1,  $x_4$  has 0,  $x_5$  has 1,  $x_6$  has 0,  $x_7$  has 1,  $x_8$  has 0,  $x_9$  has 1, and  $x_{10}$  has 0. Arcs connect the following pairs of points:  $(x_1, x_2)$ ,  $(x_3, x_4)$ ,  $(x_5, x_6)$ ,  $(x_7, x_8)$ , and  $(x_9, x_{10})$ .

89

97

105

113

121

The diagram shows a horizontal line representing a signal path. On the left, it is labeled  $h14(ln)$ . There are three stages of components represented by circles on the line. The first stage has three circles. The second stage has two circles, with a '0' above the first one. The third stage has three circles, with a '1' above the first one. Curved lines connect the circles in each stage, representing signal paths or feedback loops.

129

hl4 (ln)  $\frac{0}{\mathbf{e}} \mathbf{H}$